






**Method and apparatus for determining transmission mode and synchronization for a digital audio broadcasting signal****Publication number:** TW502506 (B)**Also published as:****Publication date:** 2002-09-11 WO0079712 (A1)**Inventor(s):** GOLDSTON DON ROY [US]; MATHERNE MARCUS [US] US6556639 (B1)**Applicant(s):** IBIQUITY DIGITAL CORP [US] RU2248673 (C2)**Classification:** MXPA01013124 (A)**- international:** *H04J11/00; H04H20/46; H04L27/26; H04L1/00; H04J11/00; H04L27/26; H04L1/00; (IPC1-7): H04H1/00; H04L27/26* JP2003502939 (T)**- European:** H04L27/26M1P; H04H20/30; H04H60/07; H04L1/00B7V; H04L5/00C4A

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**Application number:** TW20000112413 20000623**Priority number(s):** US19990339363 19990624**Abstract of TW 502506 (B)**

A method is provided for transmitting control information in a digital audio broadcasting system. The method comprises the steps of transmitting a plurality of control bits in each of a plurality of control frames, wherein a first sequence of the control bits represents a transmission mode, and a second sequence of the control bits a control data synchronization word. The plurality of control bits can further include a third sequence of bits representative of an interleaver synchronization word. A method performed in a radio receiver for determining transmission mode and synchronization for a digital audio broadcasting signal is also provided. The method comprises the steps of receiving a plurality interleaver frames containing digital information, wherein each of the interleaver frames includes a plurality control frames. The control frames include a plurality of control bits, wherein a first sequence of the control bits represents a transmission mode, a second sequence of the control bits a control data synchronization word. The plurality of control bits can further include a third sequence of bits representative of an interleaver synchronization word. The first sequence of control bits is processed to determine a transmission mode; the second sequence of control bits is processed to determine control data synchronization; and the third sequence of control bits is processed to determine interleaver boundaries. Radio frequency transmitters and receivers that utilize the above methods are also disclosed.

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